	Туре	L#	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	50376	pll or (phase\$1 adj lock\$2 adj loop\$2)	00.711	2006/05/04 08:47
2	BRS	L2	7512	1 same (memory rom ram)	100.711	2006/05/04 08:48
3	BRS	L3	874215	interfac\$3	00.71	2006/05/04 08:49
4	BRS	L4	11075	driver near5 clock	100,711	2006/05/04 08:50
5	BRS	L5	30397	first adj2 clock	00.71	2006/05/04 08:50
6	BRS	L6	27263	second adj2 clock	00.711	2006/05/04 08:51
7	BRS	L7	67967	synchron\$5 near5 clock	00.711	2006/05/04 08:52
8	BRS	L8	673	1 same 4	00.7	2006/05/04 08:52
9	BRS	L9	191	2 and 8	00.71.	2006/05/04 08:53
10	BRS	L10	36	9 and 5 and 6 and 7	00.7	2006/05/04 09:00
11	BRS	L11	617	2.clm.		2006/05/04 09:00
12	BRS	L12	193140	3.clm.	US-PGPUB; USPAT	2006/05/04 09:00
13	BRS	L13	1652	4.clm.	00.711	2006/05/04 09:00
14	BRS	L14	14411	5.clm.	00.711	2006/05/04 09:01
15	BRS	L15	13734	6.clm.	US-PGPUB; USPAT	2006/05/04 09:01
16	BRS	L16	14050	7.clm.	0317(1	2006/05/04 09:01
17	BRS	L17	0	11 and 12 and 13 and 14 and 15 and 16	US-PGPUB; USPAT	2006/05/04 09:02